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65913 NXP, B.V. NXP INTELLEO M/S41-SJ	7590 06/07 CTUAL PROPERT	^{/2007} Y DEPARTMENT	l he Stat addi tran	Certi reby certify that this es Postal Service wi ressed to the Mail smitted to the USPT	ficate of Mailing or Trans Fec(s) Transmittal is being th sufficient postage for firs Stop ISSUE FEE address O (571) 273-2885, on the d	mission g deposited with the United st class mail in an envelope above, or being facsimile ate indicated below.
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APPLICATION NO.	FILING DATE	<u> </u>	FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/537,574	06/06/2005		Sifen Luo		US02 0509	6730
TITLE OF INVENTION: AMPLIFIER CIRCUIT HAVING AN IMPEDANCE-CONTROLLABLE BIAS-BOOSTING CIRCUIT APPLIN TYPE SMALL ENTITY ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE DATE DUE						
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE			
nonprovisional	NO	\$1400 ART UNIT	\$300 CLASS-SUBCLASS	\$0]	\$1700	09/07/2007
EXAMINER MOTTOLA, STEVEN J				J.		
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Please check the appropriate assignce category or categories (will not be printed on the patent):						
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Authorized Signature	Peter	Louren.		Date	6-NOV-2007	
Typed or printed name		Zahulski			43,305	
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